GOVT. POLYTECHNIC, NUAPADA

LESSON PLAN

Discipline: CSE	Semester: 3rd	Name of the Teaching Faculty: Guest Faculty
Subject: Computer System Architecture	No. of Class class alloted per week: 4	Semester from: 01/07/2024 No. of weeks: 15 Session: 2024-25
WEEK	**DAY**	**THEORY TOPICS**
1st	1st	Basic Structure of computer hardware
	2nd	Functional Units
	3rd	Computer components
	4th	Performance measures
2nd	1st	Memory addressing & Operations
	2nd	Memory addressing & Operations
	3rd	Introduction to Instructions & instruction Sequencing
	4th	Fundamentals to instructions
3rd	1st	Operands
	2nd	Op Codes
	3rd	Instruction formats
	4th	Addressing Modes
4th	1st	Continuing addressing modes
	2nd	Introduction to Processor System
	3rd	Register Files
	4th	Complete instruction execution
5th	1st	Complete instruction execution
	2nd	Complete instruction execution
	3rd	Hardware control
	4th	Hardware control
6th	1st	Micro program control
	2nd	Micro program control
	3rd	Revision & Questionaire
	4th	Introduction to Memory System
7th	1st	Memory characteristics
	2nd	Memory hierarchy
	3rd	RAM & ROM Organization
	4th	RAM & ROM Organization
8th	1st	Continuing about RAM and ROM organization
	2nd	Interleaved Memory
	3rd	Cache memory
	4th	Virtual memory
9th	1st	Revision & Questionaire
	2nd	Introduction to Input – Output System
	3rd	Input - Output Interface
	4th	Input - Output Interface
10th	1st	Modes of Data transfer
	2nd	Programmed I/O Transfer
	3rd	Interrupt driven I/O
	4th	DMA

11th	1st	I/O Processor
	2nd	Continuing I/O Processor
	3rd	Revision & Questionaire
	4th	Introduction to I/O Interface
12th	1st	Bus Architecture
	2nd	Bus and System Bus
	3rd	Types of System Bus
	4th	Bus Structure
13th	1st	Basic Parameters of Bus design
	2nd	Basic Parameters of Bus design
	3rd	SCSI
	4th	USB
14th	1st	Revision & Questionaire
	2nd	Revision & Questionaire
	3rd	Unit Test
	4th	Parallel Processing
15th	1st	Linear Pipeline
	2nd	Multiprocessor
	3rd	Flynn''s Classification
	4th	Unit Test

Bibhuti Bhusan Dash
Signature of
Faculty

16/08/24
HOD
Sr. Lecturer(CSE)